

**MAHATMA EDUCATION SOCIETY'S  
Pillai College of Engineering, New Panvel**

**Third Year Electronics Engineering Semester V (Old) ATKT held in the Month of May/June 2019**

Exam Seat No.	Name	MAX MIN	PP-1			PP-2			PP-3		PP-4			PP-5			PP-6		PP-7	Total 850	RESULT
			TH	TW	OR	TH	TW	PR	TH	TW	TH	TW	PR	TH	TW	OR	TH	TW	OR		
			100	25	25	100	25	25	100	25	100	25	25	100	25	25	50	25	50		
			40	10	10	40	10	10	40	10	40	10	10	40	10	10	20	10	20		
01. CONTINUOUS TIME SIGNAL&SYSTEM			02. MICROPROCESSOR& MICROCONTROLLER-I						03. ELECTROMAGNETIC ENGINEERING												
04. LINEAR INTEGRATED CIRCUITS AND DESIGN			05. DIGITAL COMMUNICATION AND CODING						06. ENVIRONMENTAL STUDIES			07. ELECTRONICS WORKSHOP-II									
<b>08E51901</b>	Kaul Mohit Mohanlal		40+	16+	19+	42+	18+	19+	AB	17+	54+	19+	18+	00F	14+	14+	25+	20+	38+	<b>373</b>	<b>FAIL</b>

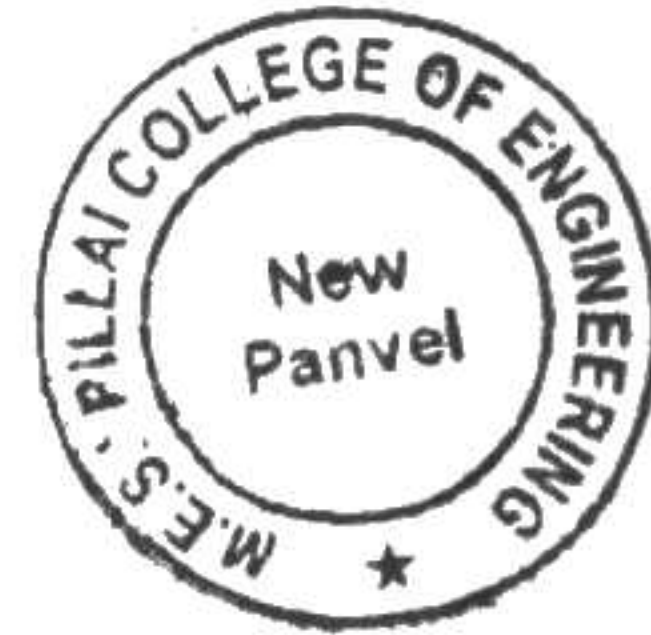
\* Equivalent Subjects are considered as per the Circular No. UG/244 of 2017-18, Dated 17th October, 2017

\*\* Marks are scaled up to out of 100 Marks as per the guidelines given in the Circular No. UG/315 of 2017-18, Dated 13th December, 2017

F - Fail    AB - Absent    # O.229    @O.5042,43&44    \*O.5045    /Female    RS - Result reserved for lower exam    R - Repeated  
+ : Marks Carried    E-Exemption can be claimed    S - Second Class    I - First Class    P-Pass Class

*Rangh*  
Prepared By

*[Signature]*  
Checked By



*[Signature]*  
Incharge of Examination

*[Signature]*  
Principal  
**PRINCIPAL**  
MAHATMA EDUCATION SOCIETY'S  
PILLAI COLLEGE OF ENGINEERING  
Dr. K. M. Vasudevan Pillai's Campus Sector 16  
New Panvel-410 206 Navi Mumbai  
Maharashtra INDIA



*[Signature]*  
23/07  
Assistant Registrar  
(Examination)  
*[Signature]*

**Final marks to be authenticated from  
PCE Examcell**