

**MAHATMA EDUCATION SOCIETY'S
Pillai College of Engineering, New Panvel**

Third Year Electronics Engineering Semester V (Old) ATKT held in the Month of November/December 2018

Exam Seat No.	Name	PP-1			PP-2			PP-3			PP-4			PP-5			PP-6			PP-7	Total	RESULT
		TH	TW	OR	TH	TW	PR	TH	TW	TH	TW	PR	TH	TW	OR	TH	TW	OR				
		MAX	100	25	25	100	25	25	100	25	100	25	25	100	25	25	50	25	50	850		
		MIN	40	10	10	40	10	10	40	10	40	10	10	40	10	10	20	10	20			
01. CONTINUOUS TIME SIGNAL&SYSTEM		02. MICROPROCESSOR& MICROCONTROLLER-I						03. ELECTROMAGNETIC ENGINEERING														
04. LINEAR INTEGRATED CIRCUITS AND DESIGN				05. DIGITAL COMMUNICATION AND CODING TECHNIQUES					06. ENVIRONMENTAL STUDIES				07. ELECTRONICS WORKSHOP-II									
08E51801	Kaul Mohit Mohanlal		40+	16+	19+	42+	18+	19+	01F	17+	54+	19+	18+	03F	14+	14+	25+	20+	38+	377	FAIL	

* Equivalent Subjects are considered as per the Circular No. UG/244 of 2017-18, Dated 17th October, 2017

** Marks are scaled up to out of 100 Marks as per the guidelines given in the Circular No. UG/315 of 2017-18, Dated 13th December, 2017


F - Fail AB - Absent # O.229 @O.5042,43&44 *O.5045 /Female RS - Result reserved for lower exam R - Repeated

+ : Marks Carried E-Exemption can be claimed S - Second Class I - First Class P-Pass Class


Prepared By


Checked By

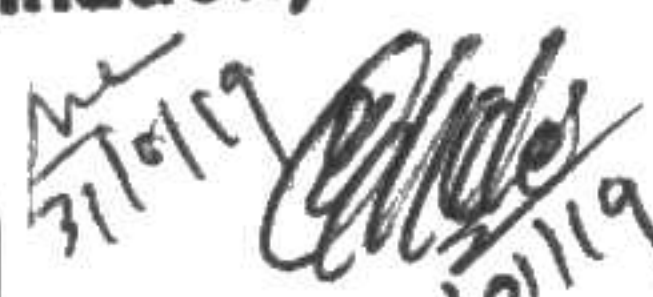



Incharge of Examination


PRINCIPAL
Principal
MAHATMA EDUCATION SOCIETY'S
PILLAI COLLEGE OF ENGINEERING
Dr. K. M. Vasudevan Pillai's Campus Sector 16
New Panvel-410 206 Navi Mumbai
Maharashtra INDIA


Assistant Registrar
(Examination)

**Final marks to be authenticated from
PCE Examcell**


31/01/19