

UNIVERSITY OF MUMBAI
OFFICE REGISTER FOR THE B.E. (ELECTRONICS ENGINEERING) (SEM VII) (REVISED COURSE) EXAMINATION HELD IN NOVEMBER 2016
COLLEGE/CENTRE : 561 PIITE FEBRUARY 15, 2017

SEAT NO.	NAME OF CANDIDATE CENTRE	PP1			PP2			PP3			PP4			PP5			PP6		TOTAL	RESULT
		WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	TW	O/P		
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	25	25	800	
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	10	10		

01. VLSI DESIGN
03. POWER ELECTRONICS AND DRIVES
05. ELECTIVE-I : 8:DIGITAL IMAGE PROCESSING DESIGN
02. FILTER DESIGN
04. COMMUNICATION NETWORKS
06. PROJECT-A

50177057	DALVI RAHUL ASHOK ARUNA	61	16+	12+	48+	18+	16+	42+	13+	16+	43+	16+	17+	60+	16+	20+	23+	20+	457	P
50177058	DESHPANDE VAIBHAV JAYANT NIRMALA	AA	13+	12+	AA	15+	17+	AA	14+	14+	41+	17+	19+	62+	14+	16+	22+	23+	299	A ABS
50177059	GHARAT AKHIL NARESH PARIN EETA	AA	14+	18+	40+	15+	20+	56+	12+	14+	40+	14+	15+	44+	12+	20+	21+	20+	375	A PPR
50177060	HATMODE MAYUR NARAYAN SAD HANA	43+	19+	18+	47	16+	19+	40	16+	17+	42	14+	18+	40+	18+	17+	18+	17+	419	P RLE
50177061	/JHA KAVITA VINAYKANT USHA	AA	16+	19+	51+	20+	17+	44+	17+	19+	56+	18+	18+	46+	24+	16+	21+	21+	423	A ABS
50177062	/KARKERA BHAVYA LINGAPPA R OHINI	44+	21+	16+	AA	17+	17+	40+	16+	17+	45+	17+	17+	52+	17+	17+	16+	17+	386	A ABS
50177063	KAUL MOHIT MOHANLAL MOHAN A	AA	20+	22+	53+	17+	20+	00F	17+	20+	AA	15+	24+	AA	14+	20+	24+	23+	289	F
50177064	KHILJI REHAN PARVEZ AKHTA R SHABANA	AA	15+	16+	41+	16+	17+	40+	16+	17+	44E	15+	15+	47+	17+	15+	16+	19+	366	F
50177065	LAROKAR HARSHAD RAMESHWAR CHHAYA	20F	16+	13+	83+	14+	17+	40E	18+	17+	40+	14+	15+	40+	21+	14+	18+	17+	417	F
50177066	MAHAMUNI PUSHKAR VASUDEO KIRTIMANGAL	AA	19+	16+	50E	20+	20+	40+	18+	17+	AA	20+	20+	47+	19+	20+	21+	19+	366	F
50177067	/OJHA SANCHITA SAMARENDRAN ATH SMRITIKANA	41	18+	19+	43+	19+	19+	42+	15+	19+	40+	18+	19+	51+	18+	19+	21+	19+	440	P RLE
50177068	PARAB FAIRAM RAMDAS DIPAS HRI	AA	14+	17+	40+	19+	19+	28F	15+	17+	40+	19+	18+	65+	18+	19+	22+	22+	392	F
50177069	PARANJAPE SAGAR ASHOK ASH WENI	15F	17+	20+	40+	16+	21+	40E	13+	15+	41+	20+	17+	40+	15+	17+	23+	21+	391	F
50177070	SHUKLA SUDHANSHU SANJAY S UDHA	43E	14+	13+	41+	20+	18+	27F	17+	18+	40+	17+	19+	55+	18+	17+	16+	17+	410	F

#:0.229; @:0.5042; *:0.5045;RCC:0.5050; +:MARKS CARRIED; /:FEMALE;NULL:NULL & VOID AS NO MARKS IN T.W.;P:PASSES;F:FAILS;A:ABSENT;
--:NOT APPLICABLE;E:EXEMPTION CAN BE CLAIMED; RR:RESERVED; ADC:ADMN. CANCELLED; PPR: PASSED PREVIOUSLY; RLE- LOWER EXAM NOT CLEAR;
~ : DYSLEXIA BENEFIT

UNIVERSITY OF MUMBAI
OFFICE REGISTER FOR THE B.E. (ELECTRONICS ENGINEERING) (SEM VII) (REVISED COURSE) EXAMINATION HELD IN NOVEMBER 2016
COLLEGE/CENTRE : 561 PIITE FEBRUARY 15, 2017

SEAT NO.	NAME OF CANDIDATE CENTRE	PP1			PP2			PP3			PP4			PP5			PP6		TOTAL	RESULT
		WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	TW	O/P		
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	25	25	800	
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	10	10		

01. VLSI DESIGN
03. POWER ELECTRONICS AND DRIVES
05. ELECTIVE-I : 8: DIGITAL IMAGE PROCESSING DESIGN
02. FILTER DESIGN
04. COMMUNICATION NETWORKS
06. PROJECT-A

50177071	SUJAN JOE JACOB JAIN	43+	15+	17+	AA	18+	15+	AA	16+	16+	18F	15+	19+	52E	17+	16+	17+	17+	311	F
50177086	RENAKE ANIKET APPASAHEB S UNITA	AA	12+	12+	57+	15+	18+	42+	11+	14+	42+	16+	17+	40+	14+	15+	18+	17+	360	A ABS

#:0.229; @:0.5042; *:0.5045; RCC:0.5050; +:MARKS CARRIED; /:FEMALE; NULL:NULL & VOID AS NO MARKS IN T.W.; P:PASSES; F:FAILS; A:ABSENT;
--:NOT APPLICABLE; E:EXEMPTION CAN BE CLAIMED; RR:RESERVED; ADC:ADMN. CANCELLED; PPR: PASSED PREVIOUSLY; RLE- LOWER EXAM NOT CLEAR;
~ : DYSLEXIA BENEFIT

TO BE AUTHENTICATED FROM PCE EXAMCELL