

**MAHATMA EDUCATION SOCIETY'S
Pillai College of Engineering, New Panvel**

Second Year Electronics Engineering, Semester III (OLD to CBGS) held in the Month of November/December 2017

SEAT NO	NAME	PP-1		PP-2				PP-3				PP-4		PP-5			PP-6	Total	RESULTS	
		WR	TW	WR	TW	O	P	WR	TW	O	P	WR	TW	WR	TW	O	TW			
		MAX	100	25	100	25	25	50	100	25	25	50	100	25	100	25	25	50		850
MIN	40	10	40	10	10	20	40	10	10	20	40	10	40	10	10	20				
		01. ENGINEERING MATHEMATICS-III				02. BASICS ELECTRONICS CIRCUITS				03. DIGITAL SYSTEM DESIGN-I										
		04. ELECTRICAL NETWORK ANALYSIS AND SYNTHESIS				05. CONTROL SYSTEM				06. PRINCIPLES OF COMMUNICATION TECHNIQUES										
12E31001	Patil Mandar Vilas	14F	EX	EX	EX	EX	EX	EX	EX	EX	EX	EX	EX	EX	EX	EX	EX	EX	14 / 100	FAIL
12E31002	Sharma Aakash Ramparvesh	29F	EX	EX	EX	EX	EX	EX	EX	EX	EX	EX	40E	EX	13F	EX	EX	EX	83 / 300	FAIL

* Equivalent Subjects are considered as per the Circular No. UG/244 of 2017-18, Dated 17th October, 2017

** Marks are scaled up to out of 100 Marks as per the guidelines given in the Circular No. UG/315 of 2017-18, Dated 13th December, 2017

F - Fail AB - Absent # O.229 @O.5042,43&44 *O.5045 /Female RS - Result reserved for lower exam R - Repeated

+ : Marks Carried E-Exemption can be claimed S - Second Class I - First Class P-Pass Class

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Prepared By

[Signature]
Checked By



[Signature]
Incharge of Examination

[Signature]
Principal
PRINCIPAL
MAHATMA EDUCATION SOCIETY'S
PILLAI COLLEGE OF ENGINEERING
Dr. K. M. Vasudevan Pillai's Campus Sector 16
New Panvel-410 206 Navi Mumbai
Maharashtra INDIA



[Signature]
Assistant Registrar
(Examination)
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22/11/18

**Final marks to be authenticated from
PCE Examcell**