

MAHATMA EDUCATION SOCIETY'S
Pillai College of Engineering, New Panvel

Second Year Electronics Engineering, Semester III (Old) held in the Month of November/December 2016

SEAT NO	NAME	PP-1		PP-2				PP-3				PP-4		PP-5			PP-6	Total	RESULTS
		WR	TW	WR	TW	O	P	WR	TW	O	P	WR	TW	WR	TW	O	TW		
		MAX	100	25	100	25	25	50	100	25	25	50	100	25	100	25	25		
MIN	40	10	40	10	10	20	40	10	10	20	40	10	40	10	10	20	850		
01. ENGINEERING MATHEMATICS-III		02. BASICS ELECTRONICS CIRCUITS						03. DIGITAL SYSTEM DESIGN-I											
04. ELECTRICAL NETWORK ANALYSIS AND SYNTHESIS				05. CONTROL SYSTEM				06. PRINCIPLES OF COMMUNICATION TECHNIQUES											
12E38001	Patil Mandar Vilas	22F	16+	45+	16+	18+	35+	40+	12+	13+	40+	44+	11+	43+	15+	10+	36+	416	FAIL
11E31001	Gaikar Prashant Balkrishna	40+	12+	45+	12+	12+	12+	40+	12+	12+	20+	06F	10+	46+	10+	11+	20+	320	FAIL
11E31002	K.Vinod Viswanathan	40+	11+	AB	15+	14+	36+	41+	16+	18+	38+	40E	19+	40+	21+	21+	39+	409	FAIL
11E31003	Nedumthottathuveedu Christy Joseph	40	10+	49+	11+	13+	22+	46+	10+	16+	38+	40+	10+	42+	10+	13+	29+	399	PASS

F - Fail AB - Absent # O.229 @O.5042,43&44 *O.5045 /Female RS - Result reserved for lower exam R - Repeated

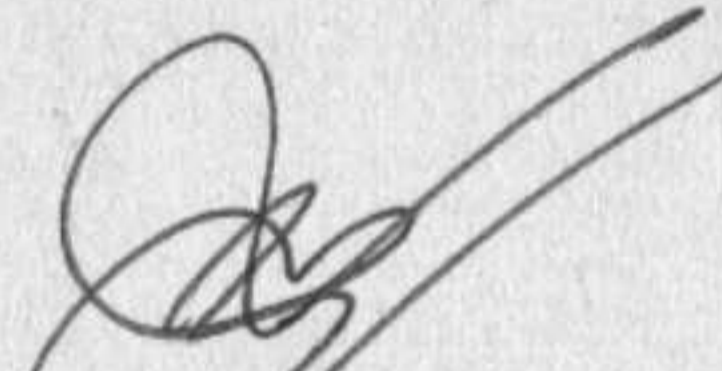
+ : Marks Carried

E-Exemption can be claimed

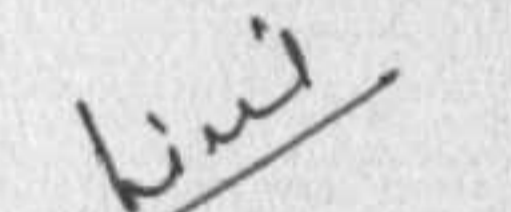
S - Second Class

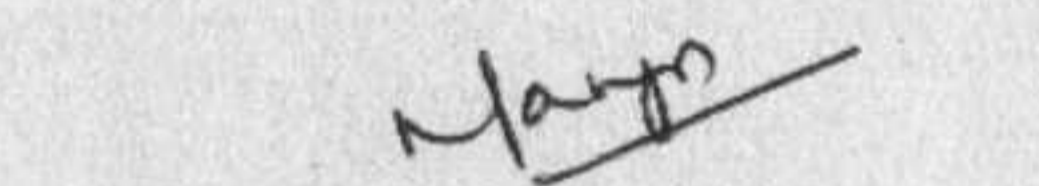
I - First Class

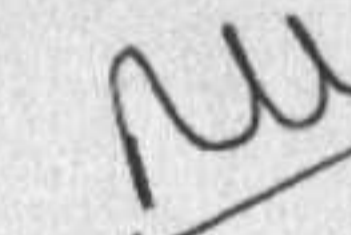
P-Pass Class


Prepared By




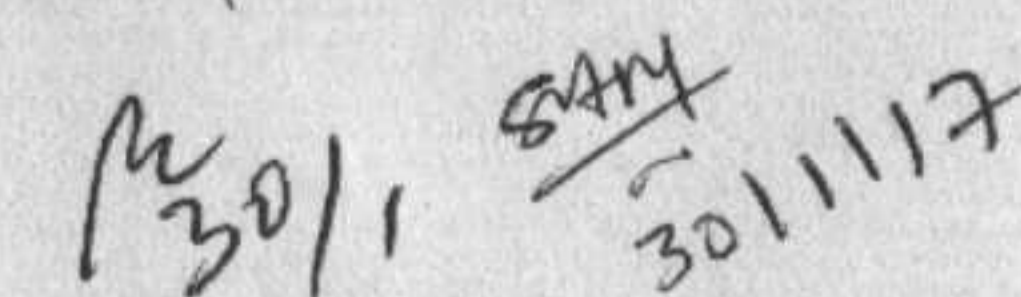

Checked By


Incharge of Examination


PRINCIPAL
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PILLAI COLLEGE OF ENGINEERING
Dr K. M Vasudevan Pillai's Campus, Sector-16
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Maharashtra, INDIA




Assistant Registrar
Engg. Unit


30/11/16

Final Marks to be authenticated from

PIIT Exam Cell