

UNIVERSITY OF MUMBAI
OFFICE REGISTER FOR THE B.E. (ELECTRONICS ENGINEERING) (SEM VII) (REVISED TO CBGS COURSE) EXAMINATION HELD IN MAY 2018
CENTRE/COLLEGE : 561 PANVEL (PIITE) JULY 30, 2018

SEAT NO.	NAME OF CANDIDATE COLLEGE	PP1			PP2			PP3			PP4			PP5			PP6		TOTAL	RESULT	
		WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	TW	O/P			
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	800	
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	40	10	10		
01. BASIC VLSI DESIGN (SEM-6)																					
03. POWER ELECTRONICS - II																					
05. ELECTIVE-I : 5:DIGITAL IMAGE PROCESSING																					
02. FILTER DESIGN																					
04. COMPUTER COMMUNICATION NETWORKS																					
06. PROJECT-A																					
50277717	KAUL MOHIT MOHANLAL MOHAN A	01F	20+	22+	53+	17+	20+	AA	17+	20+	AA	15+	24+	AA	14+	20+	24+	23+	290	F	
05. ELECTIVE-I : 8:OPTICAL FIBER COMMUNICATION																					
50277718	PARAB JAIRAM RAMDAS DIPAS HRI	49	14+	17+	40+	19+	19+	40+	15+	17+	40+	19+	18+	65+	18+	19+	22+	22+	453	P	
01. VLSI DESIGN																					
03. POWER ELECTRONICS - II																					
05. ELECTIVE-I : 38:DIGITAL IMAGE PROCESSING DESIGN																					
02. ASIC VERIFICATION																					
04. COMPUTER COMMUNICATION NETWORKS																					
06. PROJECT-A																					
50277719	/KARKERA BHAVYA LINGAPPA R OHINI	44+	21+	16+	AA	17+	17+	40+	16+	17+	45+	17+	17+	52+	17+	17+	16+	17+	386	A ABS	
50277720	MAHAMUNI PUSHKAR VASUDEO KIRTIMANGAL	40E	19+	16+	50+	20+	20+	40+	18+	17+	AA	20+	20+	47+	19+	20+	21+	19+	406	F	
50277721	RENAKE ANIKET APPASAHEB S UNITA	40	12+	12+	57+	15+	18+	42+	11+	14+	42+	16+	17+	40+	14+	15+	18+	17+	400	P	
#:O.229; @:O.5042; *:O.5045;RCC:O.5050; +:MARKS CARRIED; /:FEMALE;NULL:NULL & VOID AS NO MARKS IN T.W.;P:PASSES;F:FAILS;A:ABSENT; --:NOT APPLICABLE;E:EXEMPTION CAN BE CLAIMED; RR:RESERVED; ADC:ADMN. CANCELLED; PPR: PASSED PREVIOUSLY; RLE- LOWER EXAM NOT CLEAR; ~ : DYSLEXIA BENEFIT																					